

TO: Jerry Samson

CC: Jerry Rogers

John Hughes
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FM: Karl Guttag

RE: VDP spec

I have looked at the New UDP DATA manual and find the connection specification for the UDP to DRAM as terrible as ever (since nothing has been done). ~~The~~ attitude taken by marketing on this issue is irresponsible to say the least.

I have personally dealt with too many customers that have not gotten their systems to work without help simply due to the spec. problem in this area.

The spec must clearly indicate how to connect the UDP to the DRAMs. While an application note appended to the spec will help we must clearly indicate how to connect the DRAM's in the main body of the spec.

The reason for the confusion is the very unnatural way the ~~UDP~~ VDP connects ~~to~~ to the DRAM ~~to~~ the 9900 industry non-standard connects.

My proposal is ~~to~~ in two parts, the first part is the minimum we must do and the second is what we should do: ~~Use this -~~ (see appended table)

1) We must include a table ¹ in the data manual on the pinout page that clearly indicates how to connect the UDP's AD pins to the memories Address pins. I suggest we include an errata page with all current ~~errors~~ data manuals until the manual is fixed.

2) We should give an alternate pinout numbering for the UDP's pin that conforms to industry standard numbering for Dynamic RAMS

I have deliberately worded this memo strongly since I have ~~consistently~~ ~~and~~ repeatedly tried to get marketing to remedy this problem and they have consistently indicated that ~~it~~ it was the user's problem. In future I will be responsive ~~to~~ to customers ^{and RTCs} having ^{real} technical problems with the UDP but I will refer all questions of pinout to marketing since it is their problem.

Regards

Karl Guttag